



Hands-on training: Getting started with PIC design

Are you new to photonic integrated circuits (PIC) design? Sign up for one of the upcoming **hands-on getting started training events**. The first three dates for 2022 are open for registration, available in multiple languages and time zones!

DATE	TIME	LANGUAGE	REGISTRATION
13 Jan 2022	15:00 <i>Europe/Brussels</i> 8AM <i>US/Central</i>	English	REGISTER
13 Jan 2022	14:30 <i>Asia/Shanghai</i>	Chinese	REGISTER
17 Feb 2022	21:00 <i>Europe/Brussels</i> 2PM <i>US/Central</i>	English	REGISTER
17 Feb 2022	19:30 <i>Asia/Shanghai</i>	Chinese	REGISTER
17 Mar 2022	15:00 <i>Europe/Brussels</i> 8AM <i>US/Central</i>	English	REGISTER
17 Mar 2022	14:30 <i>Asia/Shanghai</i>	Chinese	REGISTER

IPKISS 3.7.1: Euler bends & Relative waypoints

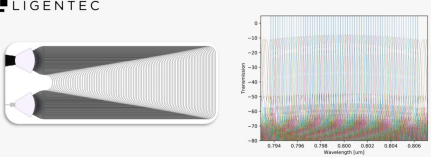
Low-loss bends? Small device footprint? Simple! You can now create Euler bends in your circuits using the new **IPKISS Euler rounding algorithm**. Euler bends are compatible with all the IPKISS connectors, such as `i3.ConnectManhattan`, `i3.ConnectBend`, etc.

In addition, we introduced the possibility of defining **relative waypoints** when you specify control points for your waveguide routes. This enables you to avoid manual calculations, making it easier to build complex routes.

Sign in or sign up on the Luceda customer portal to download it.

UPGRADE

Tutorials

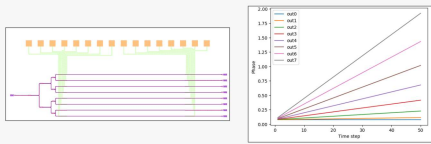


AWG FILTER
FOR OPTICAL COHERENCE TOMOGRAPHY
Powered by the IPKISS PDK for Ligentec

AWG filter for Optical Coherence Tomography

Design an arrayed waveguide grating (AWG) filter for Optical Coherence Tomography using the IPKISS PDK for Ligentec AN150.

[VIEW >>](#)

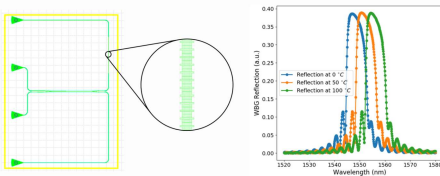


OPTICAL PHASED ARRAY
PARAMETRIC CONTROL IN
LAYOUT & SIMULATION

Optical Phased Array

Learn how to maximize parametric control in layout and simulation of an optical phased array (OPA).

[VIEW >>](#)

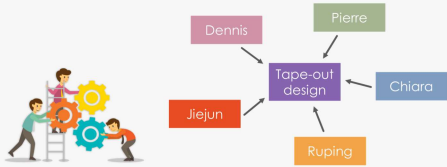


TEMPERATURE SENSING
WITH WAVEGUIDE BRAGG GRATINGS

Temperature sensing with waveguide Bragg gratings

Learn how to design, simulate and optimize an integrated temperature sensor using SOI technology.

[VIEW >>](#)

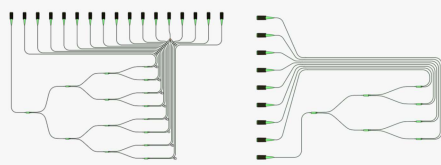


WORKING AS A TEAM
Several contributions to one tape-out run

Working as a team: tape-outs

Learn how to collaborate with your team towards a tape-out design project.

[VIEW >>](#)



ADVANCED ROUTING

Automate your waveguide routing with routing heuristics

Advanced routing

Learn how to define a routing heuristic to automate waveguide routing.

[VIEW >>](#)

Sign up on the Luceda customer portal
to download the Luceda Academy samples

[SIGN UP](#)

Support

Do you need help? Or do you have any question? The Luceda team is here for you!

Send an e-mail to support@lucedaphotonics.com. You will automatically get a reply, asking you to choose a password, so that you can track the progress of your tickets and those of your team on <http://support.lucedaphotonics.com>



[Unsubscribe](#) | [Contact](#)

©2021 All Rights Reserved